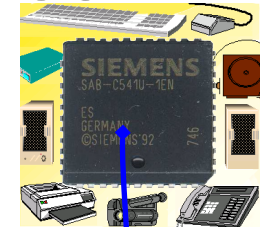


USB



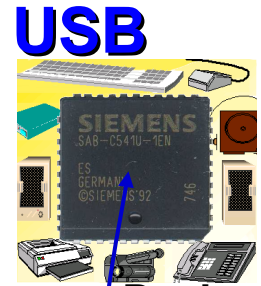
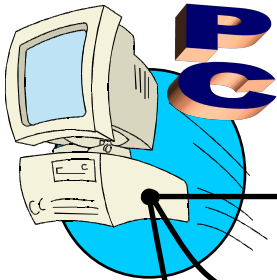
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x0DD	0x15	001

Sync	SETUP	ADDR	ENDP	CRC5	EOP
00000001	0xB4	0x00	0x0	0x08	001

Sync	DATA0	DATA			CRC16	EOP
00000001	0xC3	80 06 00 01 00 00 40 00			0xBB29	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



Device

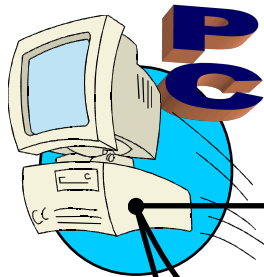
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x0DE	0x17	001

Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x00	0x0	0x8	001

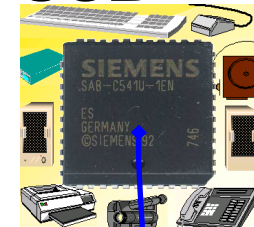
Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2	12 01 00 01 00 00 00 08	0xC8E7	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



USB



Device

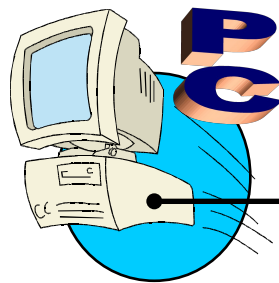
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x0E0	0x0E	001

Sync	OUT	ADDR	ENDP	CRC5	EOP
00000001	0x87	0x00	0x0	0x08	001

Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2		0x0000	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF

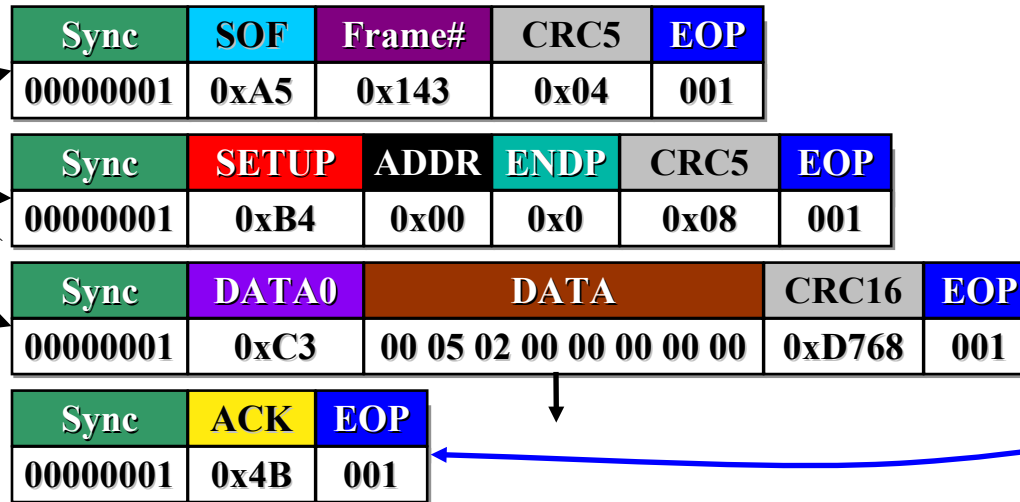
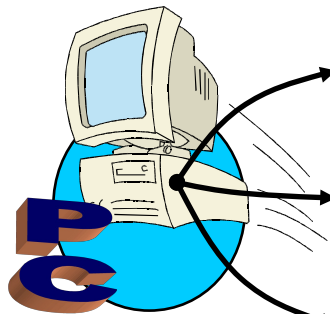


RESET
128160



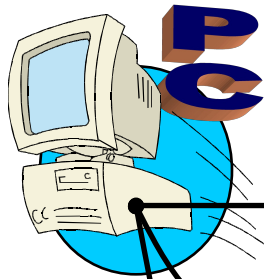
Reset initiated from host

URSTIF

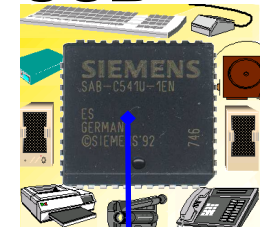


05 = set address

TRNIF



USB



Device

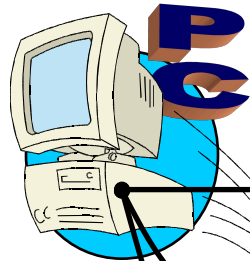
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x144	0x08	001

Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x00	0x0	0x08	001

Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2		0x0000	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x151	0x1E	001

Sync	SETUP	ADDR	ENDP	CRC5	EOP
00000001	0xB4	0x02	0x0	0x15	001

Sync	DATA0	DATA	CRC16	EOP
00000001	0xC3	80 06 00 01 00 00 12 00	0x072F	001

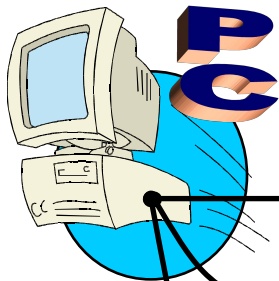
Sync	ACK	EOP
00000001	0x4B	001

USB

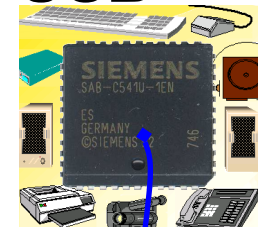


Device

TRNIF



USB



Device

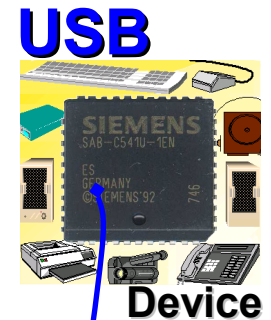
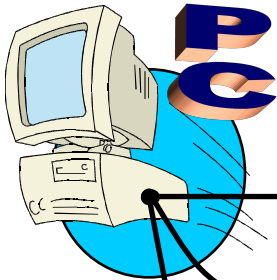
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x152	0x1C	001

Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x02	0x0	0x15	001

Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2	12 01 00 01 00 00 00 08	0xC8E7	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x153	0x03	001

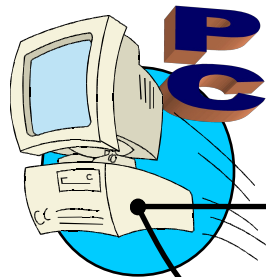
Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x02	0x0	0x15	001

Sync	DATA0	DATA	CRC16	EOP
00000001	0xC3	8B 05 41 C5 01 00 00 00	0x86DB	001

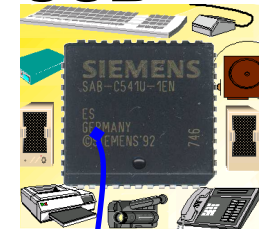
Sync	ACK	EOP
00000001	0x4B	001

TRNIF

Data stage



USB



Device

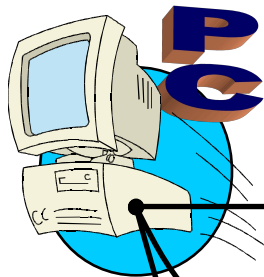
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x154	0x1D	001

Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x02	0x0	0x15	001

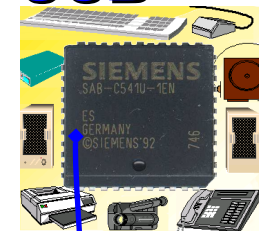
Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2	00 01	0xFCF1	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



USB



Device

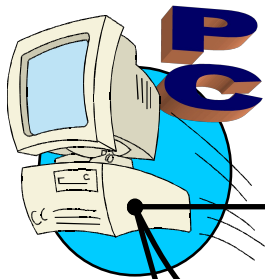
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x155	0x02	001

Sync	OUT	ADDR	ENDP	CRC5	EOP
00000001	0x87	0x02	0x0	0x15	001

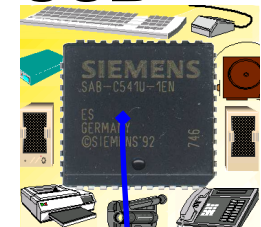
Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2		0x0000	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



USB



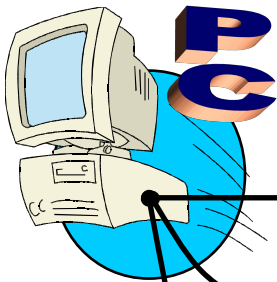
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x157	0x1F	001

Sync	SETUP	ADDR	ENDP	CRC5	EOP
00000001	0xB4	0x02	0x0	0x15	001

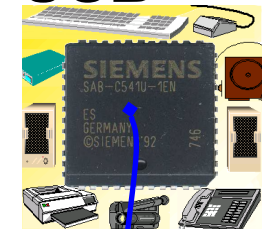
Sync	DATA0	DATA	CRC16	EOP
00000001	0xC3	80 06 00 02 00 00 FF 00	0x9725	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



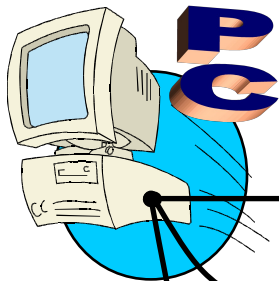
USB



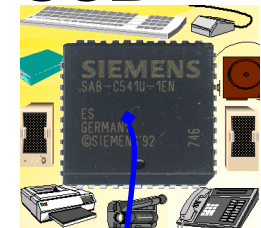
Device

Sync	SOF	Frame#	CRC5	EOP		
00000001	0xA5	0x158	0x0F	001		
Sync	IN	ADDR	ENDP	CRC5	EOP	
00000001	0x96	0x02	0x0	0x15	001	
Sync	DATA1	DATA			CRC16	EOP
00000001	0xD2	09 02 19 00 01 01 00 60			0x5020	001
Sync	ACK	EOP				
00000001	0x4B	001				

TRNIF



USB



Device

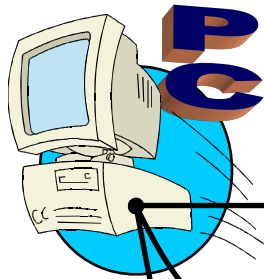
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x159	0x10	001

Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x02	0x0	0x15	001

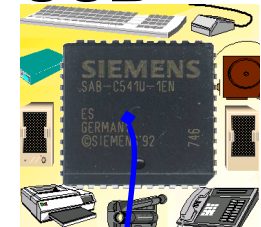
Sync	DATA0	DATA	CRC16	EOP
00000001	0xC3	32 09 04 00 00 01 00 00	0x273E	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



USB



Device

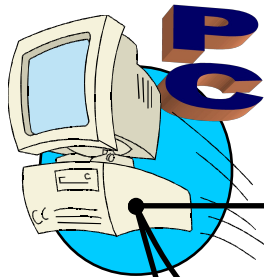
Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x15B	0x0D	001

Sync	IN	ADDR	ENDP	CRC5	EOP
00000001	0x96	0x02	0x0	0x15	001

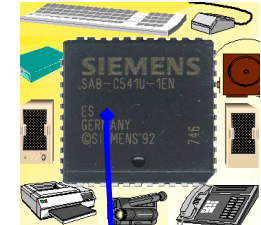
Sync	DATA0	DATA	CRC16	EOP
00000001	0xC3	00	0x0000	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF



USB



Device

Sync	SOF	Frame#	CRC5	EOP
00000001	0xA5	0x15C	0x15	001

Sync	OUT	ADDR	ENDP	CRC5	EOP
00000001	0x87	0x02	0x0	0x15	001

Sync	DATA1	DATA	CRC16	EOP
00000001	0xD2		0x0000	001

Sync	ACK	EOP
00000001	0x4B	001

TRNIF